

EV182661116

#18

11/19/02
✓8

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 09/118,359
Filing Date July 17, 1998
Inventor J. Dennis Keller et al.
Assignee Micron Technology, Inc.
Group Art Unit 2823
Examiner M. Estrada
Attorney's Docket No. MI22-587
Title: Methods of Enhancing Data Retention of a Floating Gate Transistor, Methods of
Forming Floating Gate Transistors, and Floating Gate Transistors

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

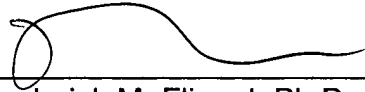
Reference - - See attached Form PTO-1449

In compliance with 37 C.F.R. §§ 1.56, 1.97 and 1.98, your attention is directed to the United States patent listed on the attached Form PTO-1449. No admission is made regarding whether the submitted reference is prior art.

Citation of this reference is respectfully requested.

Respectfully submitted,

Date: Oct. 28, 2002


Frederick M. Fliegel, Ph.D.
Reg. No. 36,138
Wells, St. John, Roberts, Gregory & Matkin P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828
(509) 624-4276